

IN THE CLAIMS:

Please cancel claim 1 without prejudice.

Please add the following new claims:

21. (New) A method for receiving data, comprising:
receiving via a receiving device a signal, the signal having a constant frequency at least part of the time, the receiving device including a first readout device and a second readout device, the signal being read via the first readout device and being read via the second readout device, the reading via the second readout device being offset in time relative to the reading by the first readout device via at least one switching period of the first readout device and the second readout device, each of the first readout device and the second readout device providing an output signal;
checking validity of the output signal of the first readout device and the output signal of the second readout device; and
selecting as a valid signal one of the output signal of the first readout device and the output signal of the second readout device as a function of the checking.
22. (New) The method according to claim 21, wherein the signal is read by the first readout device in a first clock pulse at the frequency, and the signal is read by the second readout device in a second clock pulse at the frequency.
23. (New) The method according to claim 22, wherein a phase of the first clock pulse is shifted by half a period with respect to a phase of the second clock pulse.
24. (New) The method according to claim 22, wherein the first clock pulse and the second clock pulse are defined by a

square signal, and the second clock pulse is obtained by inversion of the first clock pulse.

25. (New) The method according to claim 21, wherein the signal is read by the first readout device with a first clock edge and the signal is read by the second readout device with a second clock edge.

26. (New) The method according to claim 21, further comprising:

controlling a switching device using a checking device, the valid signal being selected by the switching device.

27. (New) The method according to claim 26, wherein information regarding the selected signal is stored in the checking device.

28. (New) The method according to claim 26, wherein the signal is received by the receiving device in an encoded form.

29. (New) The method according to claim 28, wherein the checking the validity step includes checking validity of a code of the signal.

30. (New) The method according to claim 21, further comprising:

receiving by the receiving device key words for determining the validity of the output signal of the first readout device and the output signal of the second readout device.

31. (New) The method according to claim 30, wherein the keywords are received after a predefinable time period.

32. (New) The method according to claim 21, further comprising:

determining a check value from data received in the signal, wherein the validity of the output signal of the first readout device and the output signal of the second readout device is determined by comparing the check value with a stored value.

33. (New) The method according to claim 21, further comprising:

outputting by a checking device an error signal if the output signal of at least one of the first readout device and the second readout device is not valid.

34. (New) The method according to claim 21, further comprising:

determining at least one of a first clock pulse and a second clock pulse from data of the signal.

35. (New) The method according to claim 26, further comprising:

receiving by a checking device data via which the validity of the output signal of the first readout device and the output signal of the second readout device is determined.

36. (New) A method for receiving data in a data bus, comprising:

receiving over the data bus via a receiving device a signal having a constant frequency at least part of the time, the receiving device including a first readout device and a second readout device, the signal being read via the first readout device and being read via the second readout device, the reading via the second readout device being offset in time relative to the reading by the first readout device via at least one switching period of the first readout device and the

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second readout device, each of the first readout device and the second readout device providing an output signal;

checking validity of the output signal of the first readout device and the output signal of the second readout device; and

selecting as a valid signal one of the output signal of the first readout device and the output signal of the second readout device as a function of the checking.

37. (New) A device for receiving data, comprising:

a first readout device reading a signal, the signal having a constant frequency at least part of the time;

a second readout device reading the signal at a time offset relative to the reading of the signal by the first readout device, each of the first readout device and the second readout device providing an output signal;

a validity checker checking validity of the output signal of the first readout device and the output signal of the second readout device; and

a selector selecting as a valid signal one of the output signal of the first readout device and the output signal of the second readout device as a function of the checking.

38. (New) The device according to claim 37, wherein the first readout device is a first shift register and the second readout device is a second shift register.

39. (New) The device according to claim 37, wherein the validity checker receives a code to check the validity of the output signal of the first readout device and the output signal of the second readout device.